

ELECTRIC DOUUBLE LAYER CAPACITORS

PRODUCT SPECIFICATION

規格書

CUSTOMER: DATE:

(客戶): (日期):2018-10-20

CATEGORY (品名) : ELECTRIC DOUBLE LAYER CAPACITORS

DESCRIPTION (型号) : DRL 2.7V10 F (φ12.5x20)

VERSION (版本) : 01

Customer P/N : /

SUPPLIER : /

SUPPLIER					
PREPARED (拟定)	CHECKED (审核)				
孟庆庆	刘渭清				

CUSTOMER					
APPROVAL	SIGNATURE				
(批准)	(签名)				

		SPECIFICATION			ALTERNATION HISTORY		ORY
	1	DRL SERII				ECORDS	
Rev.	Date	Mark	Page	Contents	Purpose	Drafter	Approver

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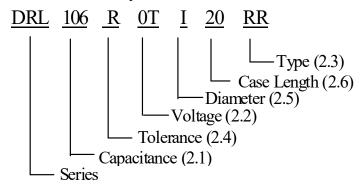
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1. Application

The specification applies to electric double layer capacitors used in electronic equipment.

2. Part Number System



2.1 <u>Capacitance code</u>

Code	106
Capacitance (F)	10

2.2 Rated voltage code

Code	0T
Voltage (W.V.)	2.7

2.3 <u>Type</u>

Code	RR
Type	Bulk

2.4 <u>Capacitance tolerance</u>

"R" stands for $0\% \sim +20\%$

2.5 <u>Diameter</u>

Code	I
Diameter	12.5

2.6 <u>Case length</u>

20=20mm

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3. Characteristics

Air Pressure

Standard atmospheric conditions

Unless otherwise specified, the standard range of atmospheric conditions for making measurements and tests is as follows:

Ambient temperature: 15°C to 35°C

Relative humidity : 25% to 75% : 86kPa to 106kPa

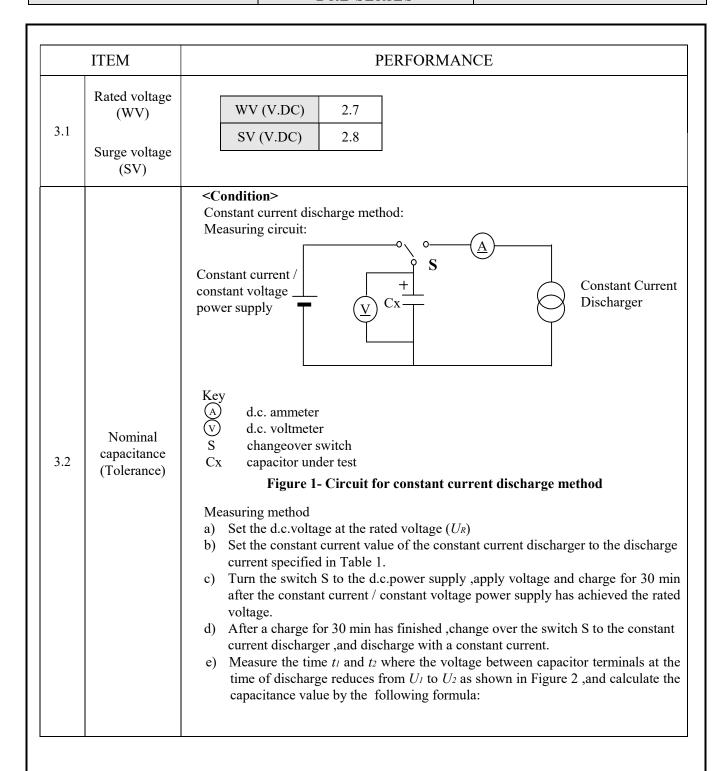
If there is any doubt about the results, measurement shall be made within the following conditions:

Ambient temperature: $20^{\circ}\text{C} \pm 2^{\circ}\text{C}$ Relative humidity : 60% to 70% Air Pressure : 86kPa to 106kPa

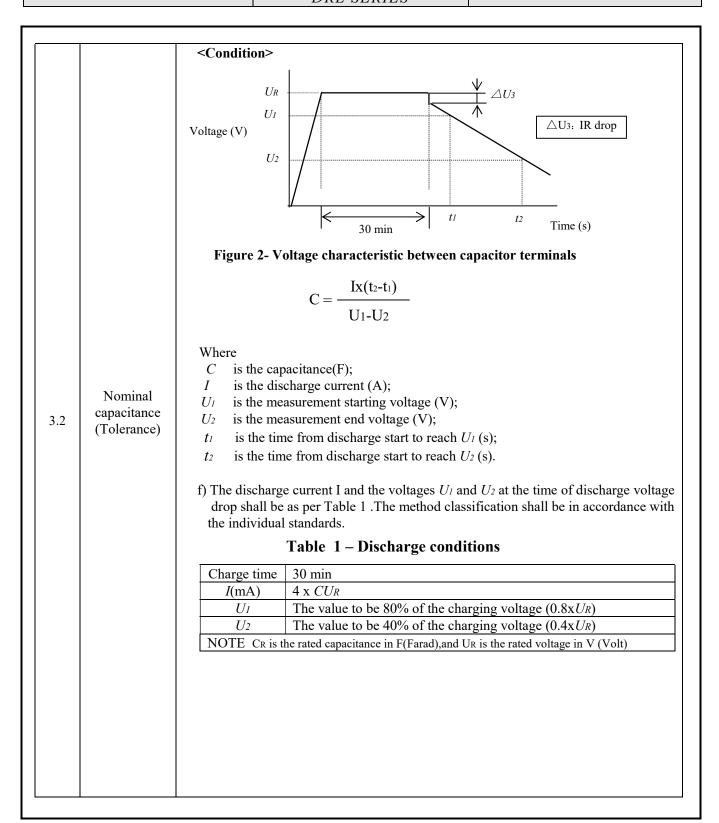
Operating temperature range

The ambient temperature range at which the capacitor can be operated continuously at rated voltage is -40°C to 70°C.

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3.3	ESR	Measur Measur <crite< th=""><th>ing frequency :1kHz ing temperature:20±2°C ring point :2mm ma wire. ria> Less than the initial limit:</th><th>x from the surface o</th><th>of a sealing resin on the lead</th></crite<>	ing frequency :1kHz ing temperature:20±2°C ring point :2mm ma wire. ria> Less than the initial limit:	x from the surface o	of a sealing resin on the lead
3.4	Leakage current	2.The e 3. Design of the second of the secon	electrification time: $72H$ stance value of protective ia> an the initial limit($25^{\circ}C \pm 1$	resistor less than 10	Σ.
		<conditi< td=""><td>1</td><td>T.</td><td>01</td></conditi<>	1	T.	01
		STEP 1	Temperature(°C) 20±2	Item Capacitance, ESR	Characteristics
			2 -40+3	△ C/C	Within ±30% of initial capacitance
		2		ESR	Less than or equal to 4 times of the value of item 3.3
3.5	Temperature	3	Keep at 15 to 35°C for 15 minutes or more		
	characteristic	4	70±2	Δ C/C	Within ±30% of initial capacitance
			7012	The limit specified in 3.3	
			-40°C/ ESR 20°C: ESR rati C 20°C: Capacitance chang	,	

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		voltage for 1000 +48/0 h	at a temperature of 70±2 °C with rated ours .The result should meet the following table:
		<criteria></criteria>	
		Item	Performance
	т 1	Capacitance Change	Within ±30% of initial capacitance
3.6	Load life	ESR	Less than or equal to 4 times of the value of item 3.3
5.0	test	Appearance	No visible damage and no leakage of electrolyte
		<condition> Humidity Test: The capacitor shall be</condition>	exposed for 240±48 hours in an atmosphere of 90~95%RH at
		40±2°C, the characteris	stic change shall meet the following requirement.
		Item	Performance
	Damp	Capacitance Change	Within ±30% of initial capacitance
3.7	heat	ESR	Less than or equal to 4 times of the value of item 3.3
	test	Appearance	No visible damage and no leakage of electrolyte

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		·		rminal in the axial direction and acting
		in a direction away from the Lead wire diameter	•	Load force (N)
		$0.5 < d \le 0.8$	i (iiiii)	10
3.8	Lead strength	b) Lead bending When the capacitor is placed table above is applied to one horizontal position and then if for 2~3 seconds. The additional bends are made to be a second to	de in the opposemm) stic shall meet to Performance Within ±30%	position and the weight specified in the he capacitor is slowly rotated 90° to a sertical position thus completing bends ite direction Load force (N) 5 The following value after a) or b) test. 6 of initial capacitance amage Legible marking and no
3.9	Resistance to vibration	Performance: Capacitance value s capacitance when the value is me	Fig2 shall not show a asured within 3	

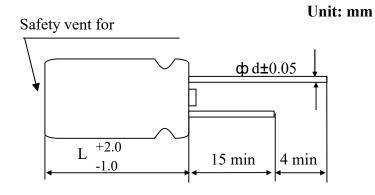
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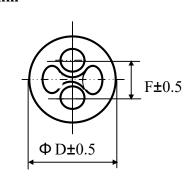
3.10	Solderability	The capacitor shall be tested under the following conditions: Solder : Sn-3Ag-0.5Cu Soldering temperature: 245±3°C Immersing time : 2.0±0.5s Immersing depth : 1.5~ 2.0mm from the root. Flux : Approx .25% rosin Performance: At least 75% of the dipped portion of the terminal shall be covered with new solder.
3.11	Resistance to soldering heat	A) Solder bath method Lead terminals of a capacitor are placed on the heat isolation board with thickness of 1.6±0.5mm. It will dip into the flux of isopropylaehol solution of colophony. Then it will be immersed at the surface of the solder with the following condition: Solder : Sn-3Ag-0.5Cu Soldering temperature : 260 ±5°C Immersing time : 5±0.5s Heat protector: t=1.6mm glass -epoxy board B) Soldering iron method Bit temperature : 350±10°C Application time : 3.5±0.5 s Heat protector: t=1.6mm glass -epoxy board For both methods, after the capacitor at thermal stability, the following items shall be measured: Item Performance Capacitance Change Within ±10% of initial capacitance Appearance No visible damage legible marking and no leakage of electrolyte

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4. Product Dimensions





φD	12.5
L	20
F	5.0
φd	0.6

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_	TA T		• 4
•		Otice	item.

- (1) The capacitor has fixed polarity.
- (2) The capacitor should be used under rated voltage.
- (3) The capacitor should not be used in the charge and discharge circuit with high frequency.
- (4) The ambient temperature affects the super capacitor life.
- (5) Voltage reduction $\Delta V=IR$ will happen at the moment of discharge.
- (6) The capacitor cannot be stored on the place with humidity over 85%RH or place with toxic gas.
- (7) The capacitor should stored in the environment within -30°C~50°C temperature and less than 60% relative humidity.
- (8) If the capacitor is applied on the double-side PCB, the connection should not be around the place on which the super capacitor can contact.
- (9) Don't twist capacitor or make it slanting after installing.
- (10) Need avoid over heat on the capacitor during soldering (The temperature should be 260°C with the time less than 5s during soldering on 1.6mm printed PCB.)
- (11) There is voltage balance problem between each capacitor unit during series connection between super capacitor.

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